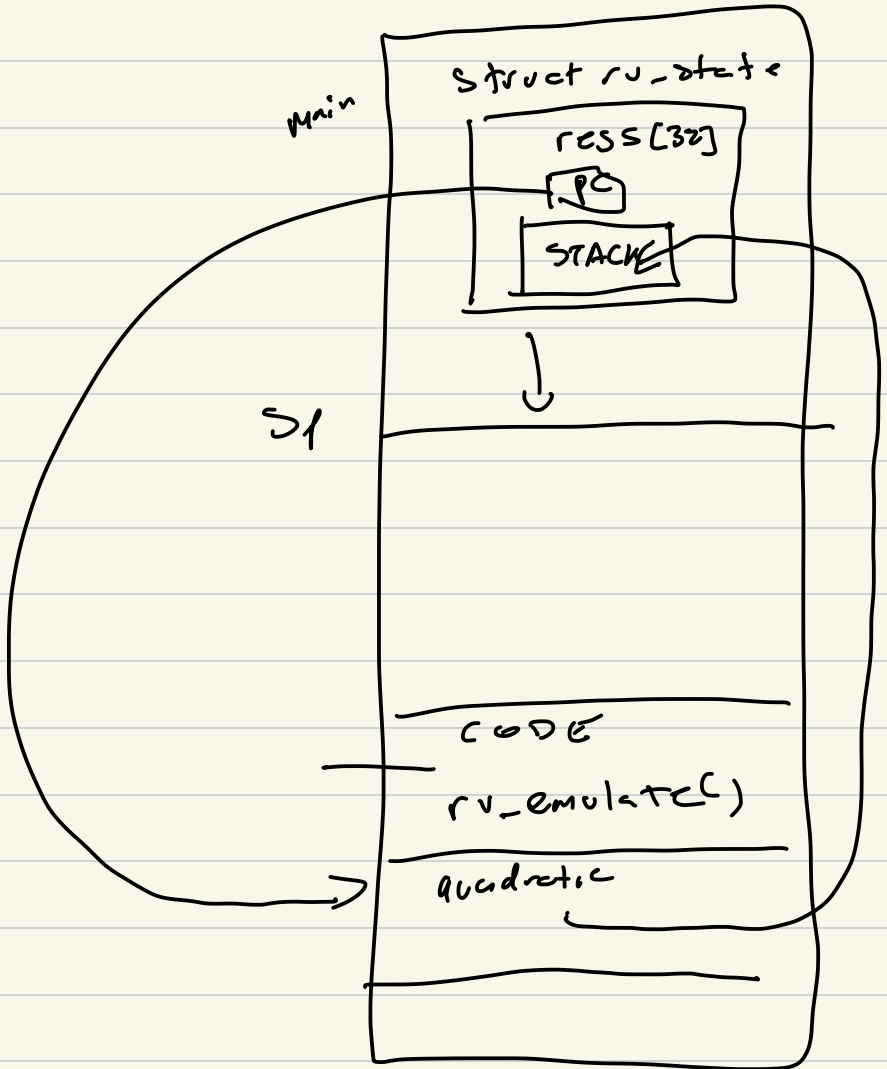


CS 315-01 Lab03 Sol Analysis Cache

Emulated stack



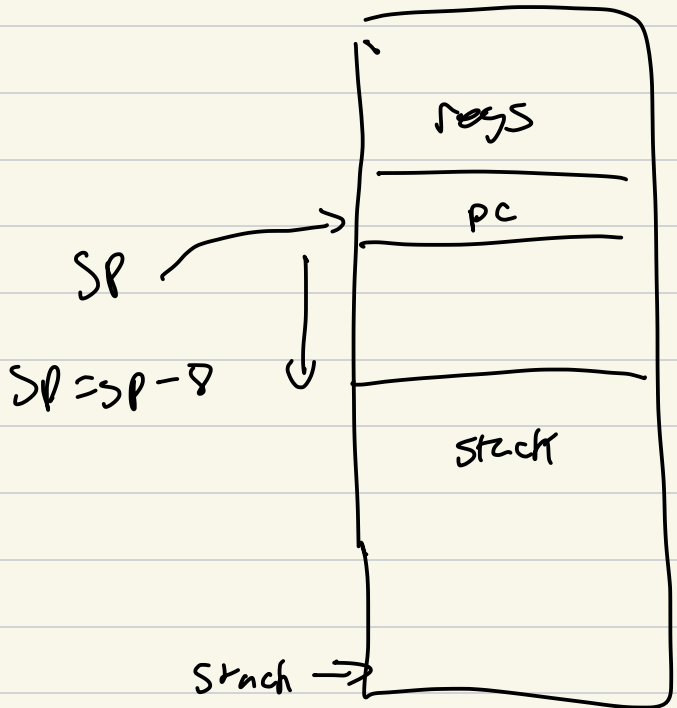
Struct ru_state {

regs

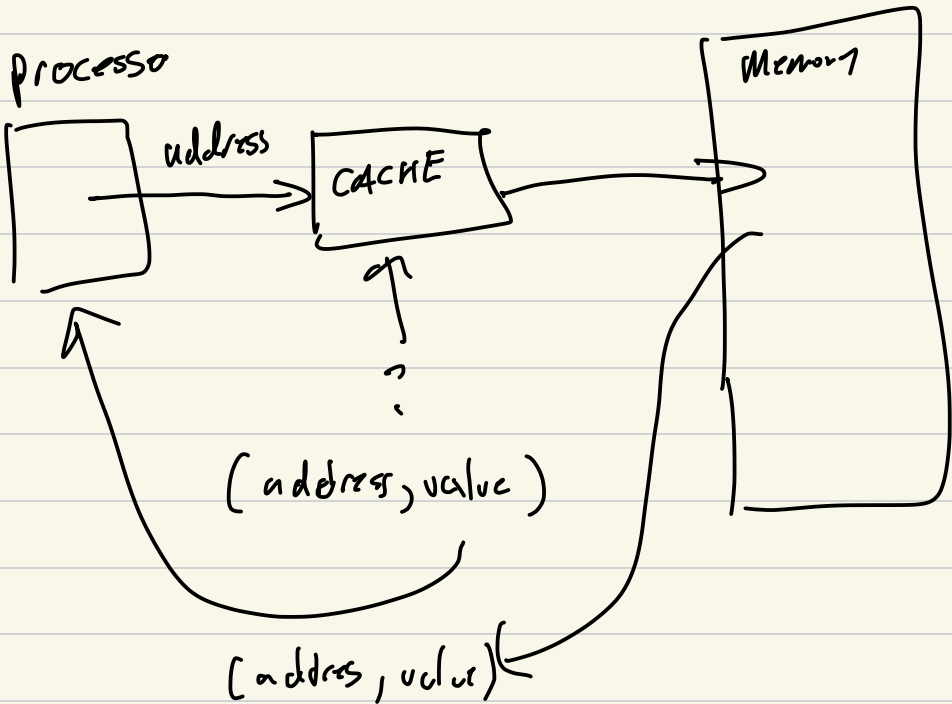
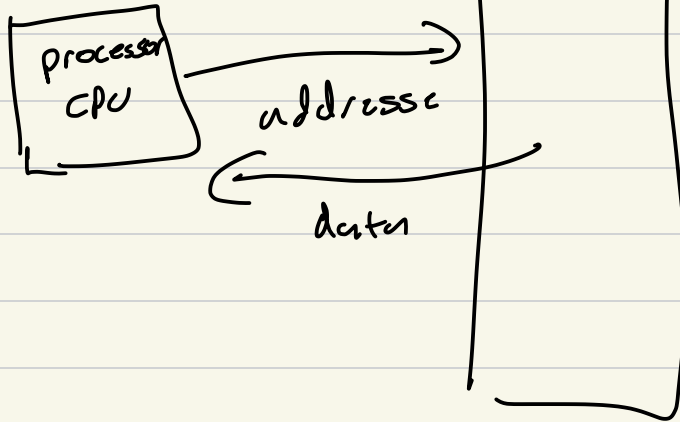
pc

uint8_t

stack [8192];



Cache Memory

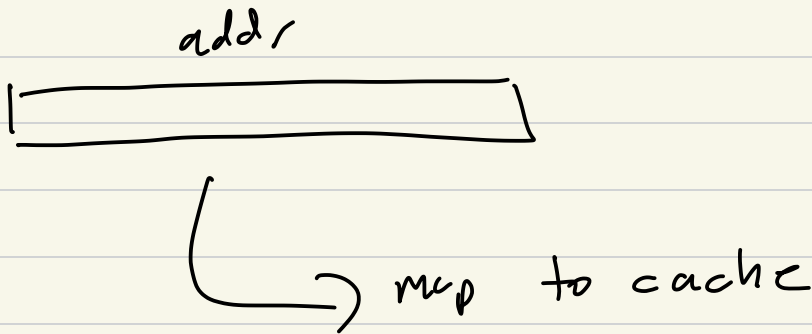


Cache Questions

Where to look given an address?

How to determine if item is the one we want.

Which item to remove if there is a conflict

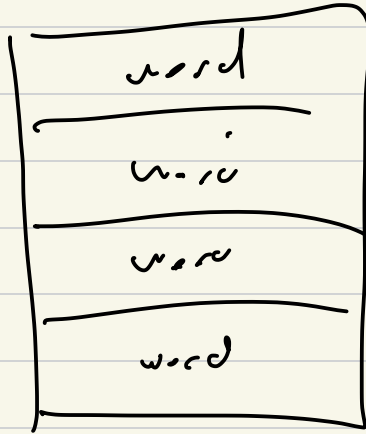


check?

- ✓ Direct Mapped
- Fully Association
- ✓ Set Associative

Block size

cache



4 slots

\$\$\$

Spatial Locality

Temporal Locality